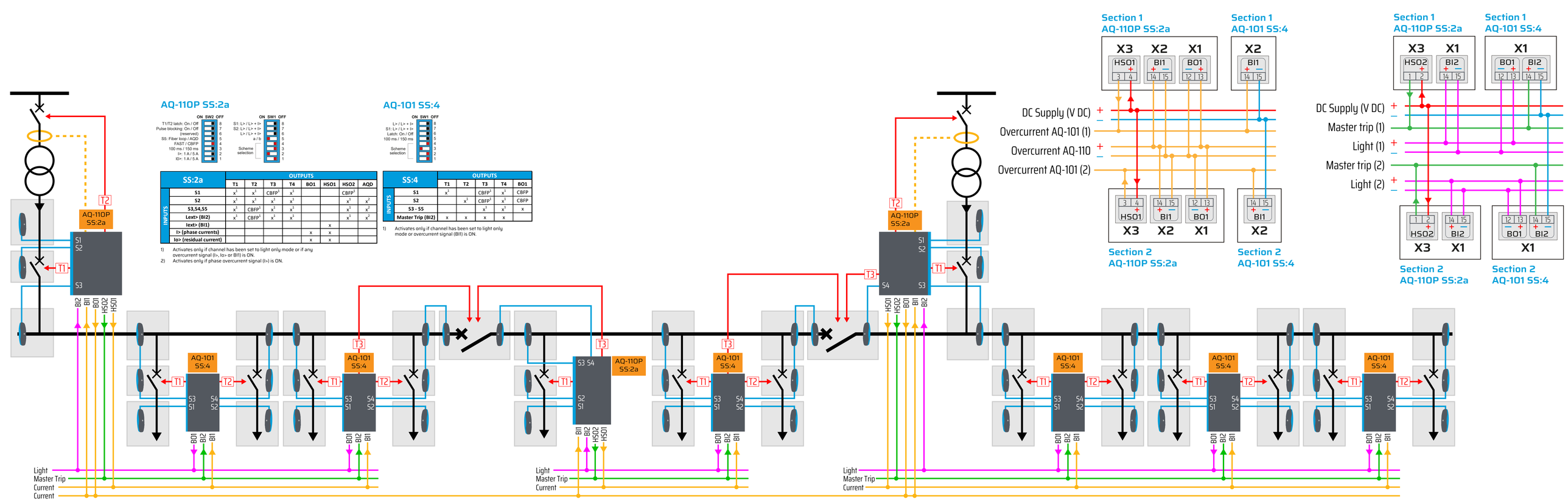
AQ-110P SS:2a
ON SW2 OFF
T1/T2 latch: On / Off
Pulse blocking: On / Off
(reserved)
SS: Fiber loop / AQD
FAST / CBFP
100 ms / 150 ms
I>: 1 A / 5 A
Io>: 1 A / 5 A
ON SW1 OFF
S1: L> / L> + I>
S2: L> / L> + I>
L> / L> + I>
a / b
Scheme selection
SS:2a
OUTPUTS
T1 T2 T3 T4 BO1 HSO1 HSO2 AQD
INPUTS
S1
S2
S3,S4,S5
Iext> (BI1)
I> (phase currents)
Io> (residual current)
CBFP
1) Activates only if channel has been set to light only mode or if any overcurrent signal (I>, Io> or BI1) is ON.
2) Activates only if phase overcurrent signal (I>) is ON.
AQ-101 SS:4
ON SW1 OFF
L> / L> + I>
S1: L> / L> + I>
Latch: On / Off
100 ms / 150 ms
Scheme selection
SS:4
OUTPUTS
T1 T2 T3 T4 BO1
INPUTS
S1
S2
S3 - S5
Master Trip (BI2)
1) Activates only if channel has been set to light only mode or overcurrent signal (BI1) is ON.
Section 1
AQ-110P SS:2a
Section 1
AQ-101 SS:4
X3 X2 X1
HSO1
BI1
BO1
X2
DC Supply (V DC)
Overcurrent AQ-101 (1)
Overcurrent AQ-110
Overcurrent AQ-101 (2)
Section 2
AQ-110P SS:2a
Section 2
AQ-101 SS:4
Section 1
AQ-110P SS:2a
Section 1
AQ-101 SS:4
X3 X1
X1
HSO2
BI2
BO1
DC Supply (V DC)
Master trip (1)
Light (1)
Master trip (2)
Light (2)
Section 2
AQ-110P SS:2a
Section 2
AQ-101 SS:4
AQ-110P SS:2a
AQ-101 SS:4
S1
S2
S3
S4
T1
T2
T3
BI2
BI1
BO1
HSO2
HSO1
Light
Master Trip
Current